

ABSTRACT OF THE DISCLOSURE

A light emitting display. A first capacitor is coupled between a gate of a first transistor and a power supply voltage. The gate thereof is coupled to a gate of a second transistor, and a data current from a data line is transmitted to the second transistor to set the gate voltages of the first and second transistors as a first voltage. A second capacitor is formed between the gates of the first and second transistors, and the data current from the data line is intercepted. Here, the first capacitor stores a second voltage by coupling of the first and second capacitors. A driving current output from the first transistor is transmitted to a light emitting element, corresponding to the second voltage.

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